CLIPPEDIMAGE= JP401307271A

PAT-NO: JP401307271A

DOCUMENT-IDENTIFIER: JP 01307271 A

TITLE: SEMICONDUCTOR DEVICE AND MANUFACTURE THEREOF

PUBN-DATE: December 12, 1989

INVENTOR-INFORMATION:

NAME

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APPL-NO: JP63137954 APPL-DATE: June 3, 1988

INT-CL (IPC): H01L029/78; H01L027/12

US-CL-CURRENT: 438/FOR.219,257/347,438/207

## ABSTRACT:

PURPOSE: To lower offset resistance by dividing a current path into the plural and forming the offset regions of each current path by semiconductor regions substantially composed of a single crystal.

CONSTITUTION: Each offset region 13 is constituted of semiconductor regions 14 substantially consisting of a single crystal. The length of the offset region 13, a distance between a gate conductor 15 and a low-resistance drain region 12, is brought to 10-20μ m, and channel width, width in the direction vertical to the flow of currents in each channel region under the gate conductor 15, is brought to 10-20μ m. The size of the offset region 13 is limited by a maximum region capable of being changed into a single crystal. A plurality of such offset regions 13 in size capable of being changed into the

single crystal are prepared, and connected in parallel,

04/22/2002, EAST Version: 1.03.0002

thus manufacturing a device having desired overall channel width, then acquiring the quantity of currents required. Accordingly, the offset regions are turned into the single crystal, and an IC including an offset type FET having high breakdown strength and low ON resistance can be manufactured.

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